MODEL 5200E TRANSMITTER

SCHEMATICS

tau-tron

A UNIT OF

GENERAL SIGNAL

MODEL 5200E
TRANSMITTER

SCHEMATICS

5200E TRANSMITTER SCHEMATICS
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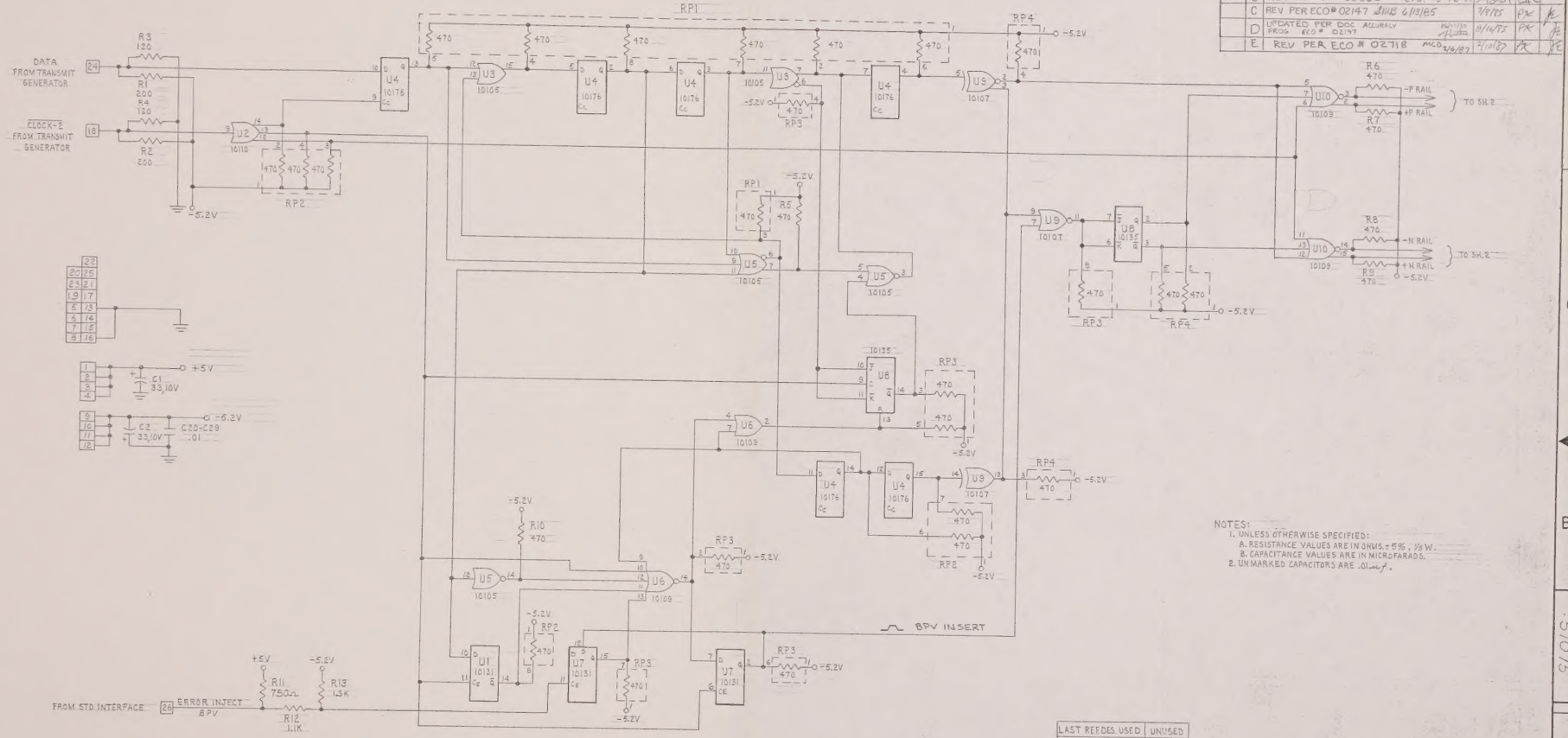
REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	1	REVISED PER PDC 00720	9-15-80	
	A	REVISED & RELEASED PER DCN00194	12-18-80	
	B	REV PER DCN 00862 C.S.	3-16-81	
	C	REV PER ECO# 02147	7/9/85	
	D	UPDATED PER DOC ACCURACY PROG ECO# 02197	8/16/85	
	E	REV PER ECO # 02718	2/10/87	



NOTES:
1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTANCE VALUES ARE IN OHMS, ±5%, 1/8 W.
   B. CAPACITANCE VALUES ARE IN MICROFARADS.
2. UNMARKED CAPACITORS ARE .01 uf.

LAST REF DES. USED	UNUSED
L2	
U12 CR3	
RP4 Q15	
R76 T2	
C36 K3	

REFERENCE DRAWINGS:
ASSEMBLY: D9545-7077
FABRICATION: D9605-7076

REFERENCE DES.	
LAST USED	UNUSED
C36	
CR3	
K3	
L2	
Q15	
R77	
RP4	
T2	
U12	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

NEXT ASSY	USED ON
TDK-57077	55200D XMTR

APPLICATION

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR NO.
DR C. Smith 1-3-80
CHK 9-7-80
APPD 9-18-80
APPROVED
BY DIRECTION OF

tau-tron inc
Chelmsford, MA. 01824

DRAWING TITLE
SCHEMATIC
OUTPUT AMPLIFIER

SIZE D | CODE IDENT NO. | DRAWING NO. 57075 | REV E
SCALE | 55200D TX | SHEET 1 OF 2

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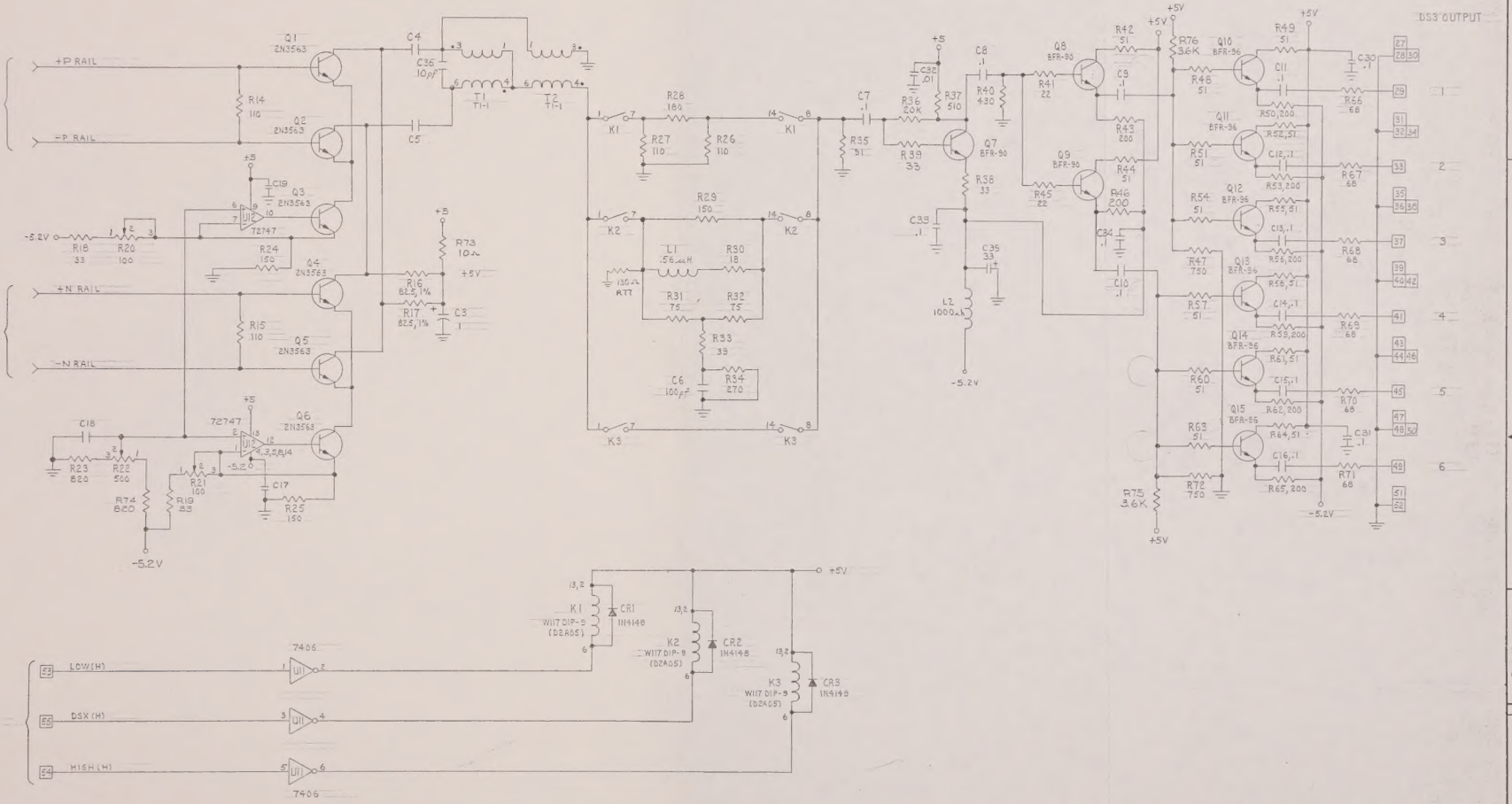
REVISIONS

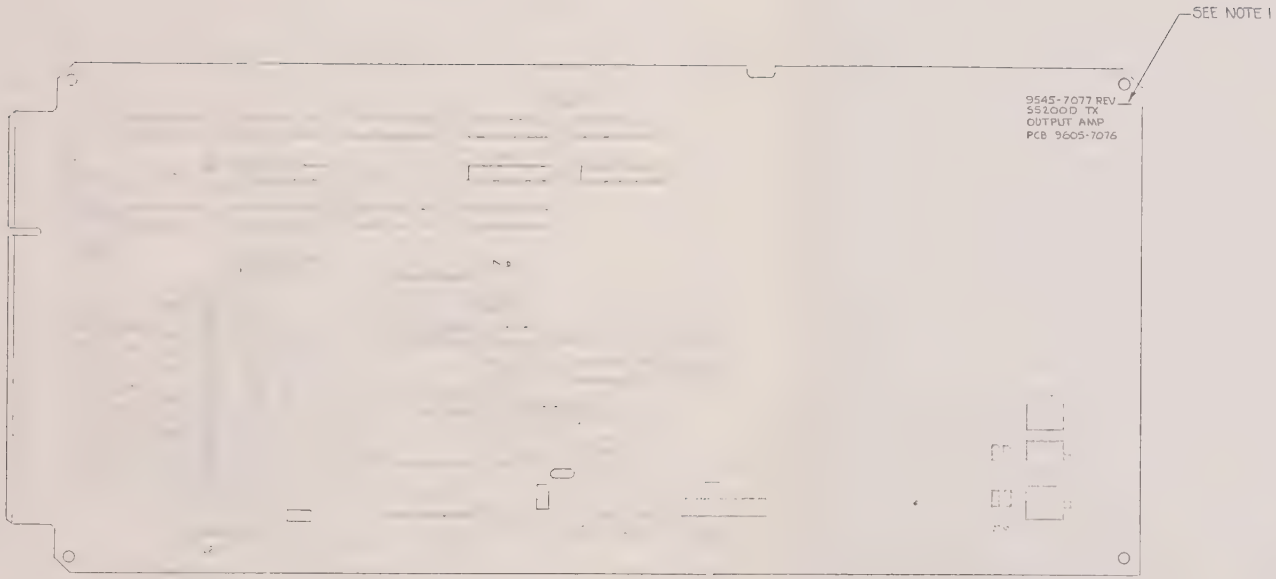
ZONE	LTR	DESCRIPTION	DATE	APPROVED
		SEE SHEET 1	9-18-80	
		SEE SHEET 1 FOR REVISIONS		



WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±	CONTR NO. DR C. Smith 4-3-80 CHK 4-7-80 APPD 9-18-80	tau-tron inc Chelmsford, Ma. 01824
TDK-57077 / S5200D XMTR	MATERIAL:	APPROVED	DRAWING TITLE: SCHEMATIC OUTPUT AMPLIFIER
NEXT ASSY / USED ON		BY DIRECTION OF	SIZE D / CODE IDENT NO. / DRAWING NO. TDJ-57075 / REV E
APPLICATION			SCALE / S5200D TX / SHEET 2 OF 2

57075

	E	REDRAWN REPLACES REV D REV PER ECO #02377 RZ. 11MAR86	3/11/86	RL



NOTES:

1. PLACE THE ASSEMBLY PARTS LIST REVISION MARK ON THE ASSEMBLY IN DESIGNATED AREA.

JT	9-16-80	
B. COOPER	9-18-80	
WEL	9-18-80	

E57154 SS200D TX

tau-tron

ASSEMBLY
OUTPUT AMPLIFIER

57077 REV E

2/1 SS200D TX 1 OF 1

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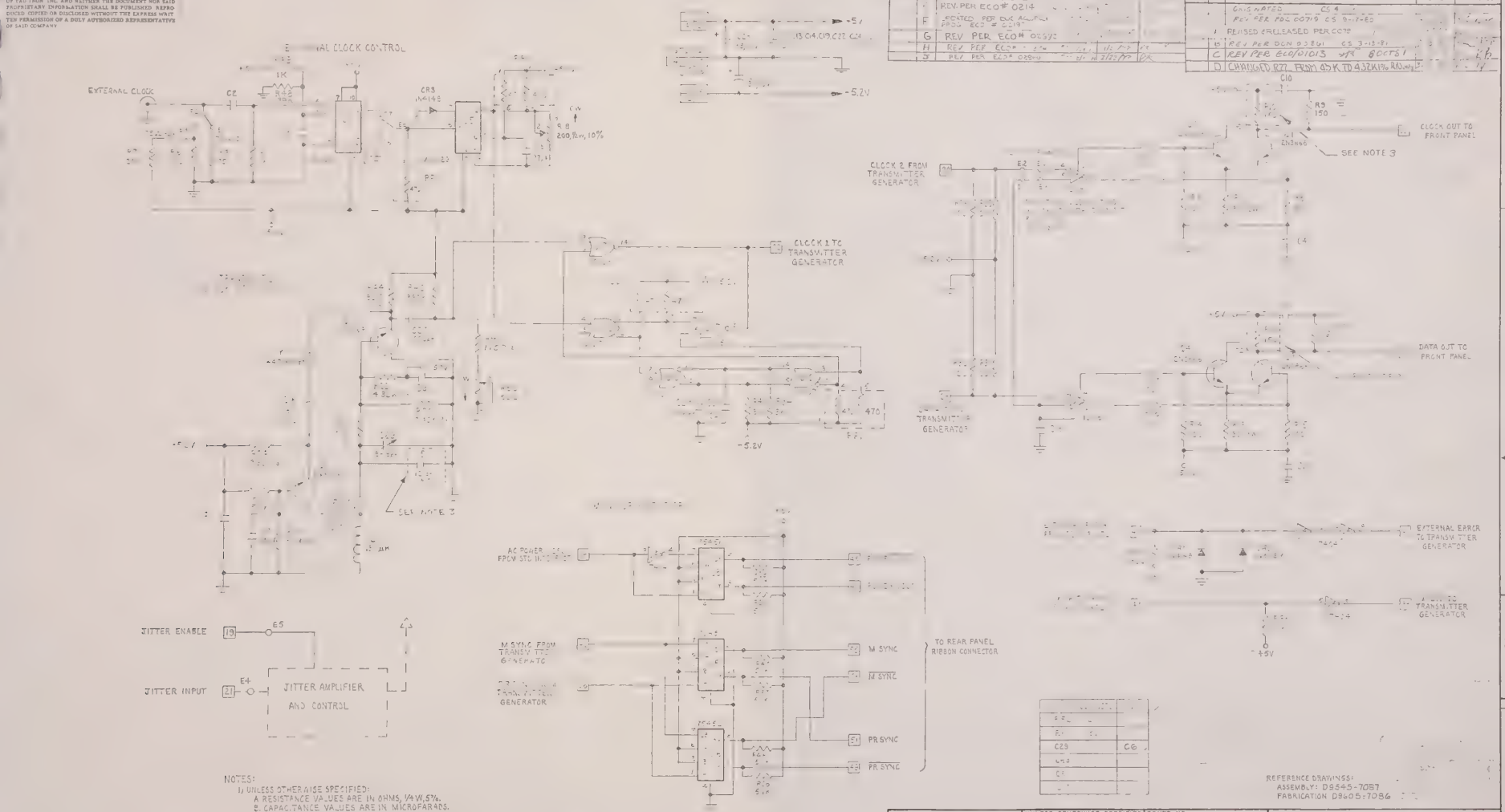
	REV. PER ECO# 0214		
F	PRINTED PER DOC ACCEPTED PROD ECO # 0219		
G	REV PER ECO# 0259		
H	REV PER ECO#		
J	REV PER ECO# 0290		

REVISIONS

	DESCRIPTION	DATE	APPROVED
	ORIGINATED CS 4		
	REV PER PDC 00719 CS 9-17-80		
	REVISED & RELEASED PER CC79		
B	REV PER DCN 00861 CS 3-13-81		
C	REV PER ECO/01013 SR 8 OCT 81		
D	CHANGED R27 FROM 45K TO 432K 1% R10		



NOTES:
1) UNLESS OTHERWISE SPECIFIED:
A. RESISTANCE VALUES ARE IN OHMS, 1/4W, 5%.
B. CAPACITANCE VALUES ARE IN MICROFARADS.
2) UNMARKED CAPACITORS ARE .01 uf.
3) COMPONENTS TO BE ADDED OR TRIMMED IN TEST AS REQUIRED.

REFERENCE DRAWINGS:
ASSEMBLY: D9545-7087
FABRICATION D9605-7086

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	CONTR NO.	tau-tron inc Chelmsford, Ma. 0824
	TOLERANCES: ANGLES ±	DR C. Smith 4-1-80	DRAWING TITLE SCHEMATIC OSCILLATOR/INTERFACE P.C.B.
	FRACTIONS ±	CHK P 4-16-80	
	3 PLACE DECIMALS ±	APPD	
	2 PLACE DECIMALS ±		
	1 PLACE DECIMALS ±		
TDK-57087 / 552000 XMTR	MATERIAL	APPROVED	SIZE D / CODE IDENT NO / DRAWING NO 57085 / REV J
NEXT ASSY / USED ON		BY DIRECTION OF	SCALE 1/1 / 552000 / SHEET
APPLICATION			

57085

H	REDRAWN, REPLACES REV G WITH CHG PER COC ACCURACY PROG ECO # 02197	

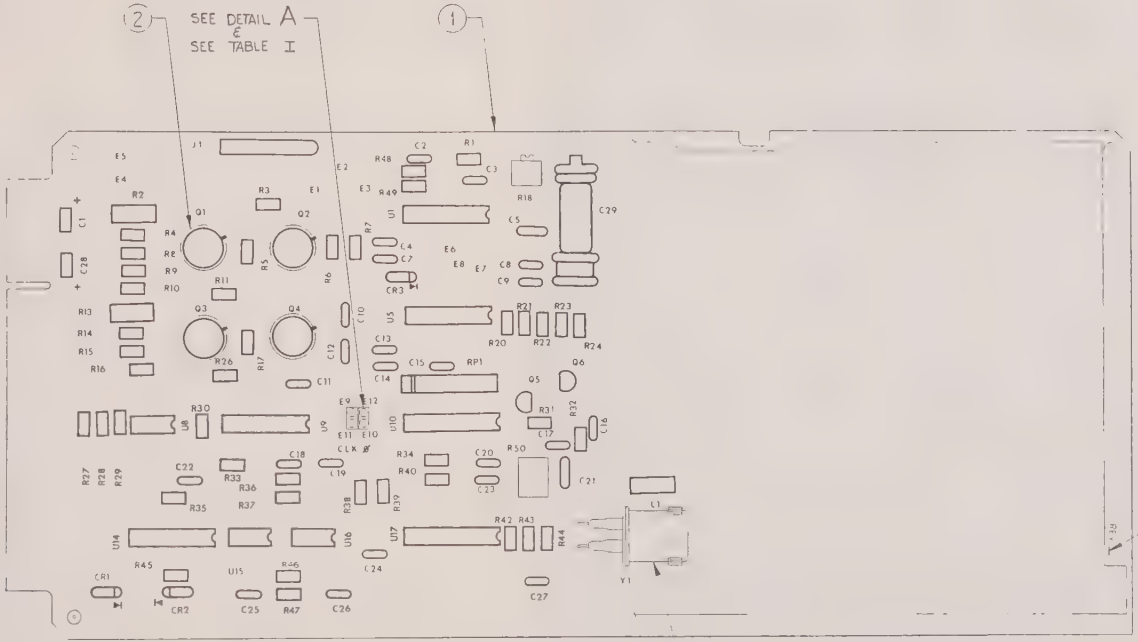
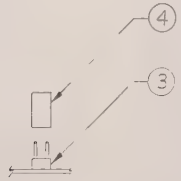


TABLE I	
JUMPERS	
FROM	TO
E9	E11
E12	E10

NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. INSTALL CRYSTAL AFTER SOCKET HAS BEEN INSTALLED.



DETAIL A

E-57158	S5200D TX	
NEXT ASSY	USED ON	
APPLICATION		

C ATHERTON	1/23/81	
B. COOPER	1/29/81	
W ROLLINS	1/29/81	
APPROVED		

tau-tron

OSCILLATOR INTFC PCB ASSY

D	954	57087	H
SCALE 2/1	S5200D TX	SHEET 1 OF 1	

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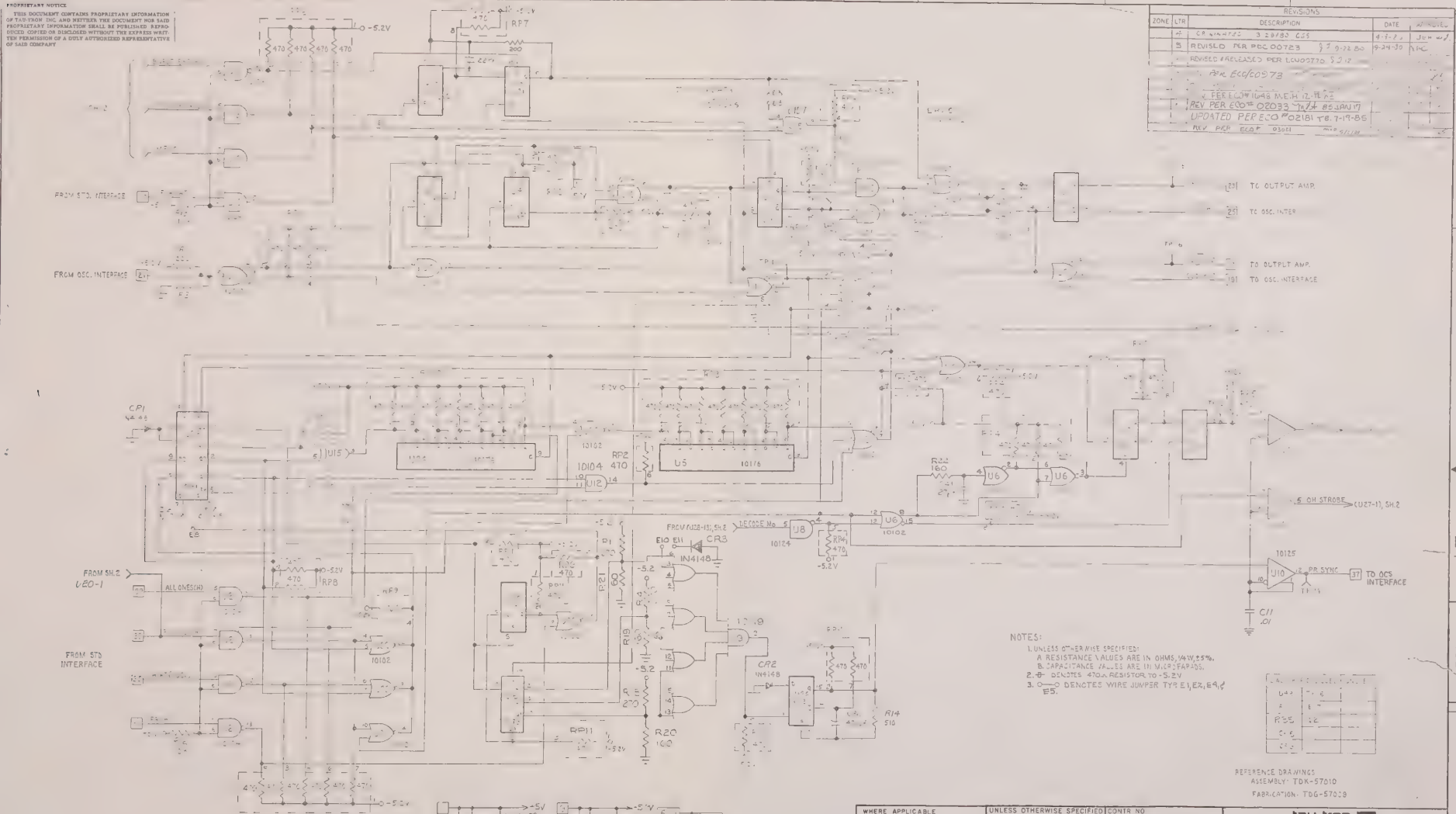
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
		CR WINKLES 3 29/80 CJS	4-7-80	JOH wd
	S	REVISED PER PCC 00723	9-24-80	JHC
		REVISED & RELEASED PER ECO 00770		
		PER ECO 00973		
		PER ECO 01048		
		REV PER ECO # 02033	85 JAN 17	
		UPDATED PER ECO # 02181	8-7-85	
		REV PER ECO # 03001		



WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	CONTR NO	tau-tron INC Chelmsford, MA 01824
	TOLERANCES. ANGLES ±	DR	DRAWING TITLE SCHEMATIC TRANSMIT GENERATOR PCB
	FRACTIONS ±	CHK	
	3 PLACE DECIMALS ±	APPD	
	2 PLACE DECIMALS ±		
	1 PLACE DECIMALS ±		
TDK-57010 / 5520Gb TX	MATERIAL:	APPROVED	SIZE / CODE IDENT NO / DRAWING NO 57008 / REV G
NEXT ASSY / USED ON		BY DIRECTION OF	SCALE / SHEET
APPLICATION			

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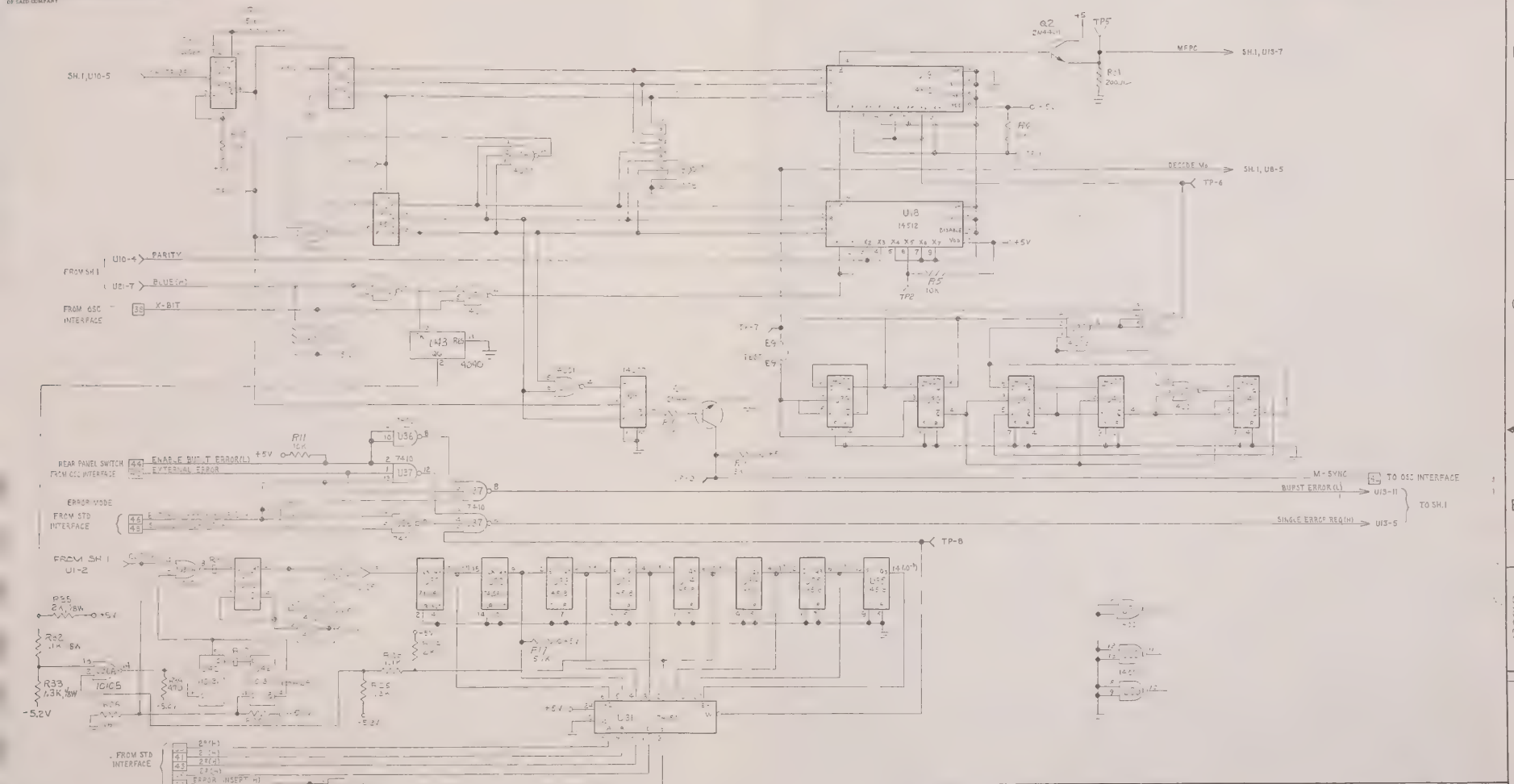
REVISI	DESCRIPTION	DATE	APPROVED
	SEE SH 1 FOR REVISIONS		



WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	CONTR NO	tau-tron INC Chelmsford MA 01824
	TOLERANCES ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±	DR	DRAWING TITLE SCHEMATIC TRANSMIT GENERATOR PCB
TDK-57010 / SE2000 TX	MATERIAL:	CHK	
NEXT ASSY / USED ON		APPROVED	SIZE D / CODE IDENT NO. / DRAWING NO. 57008 / REV E5
APPLICATION		BY DIRECTION OF	SCALE — / SE2000 TX / SHEET 2 OF 2

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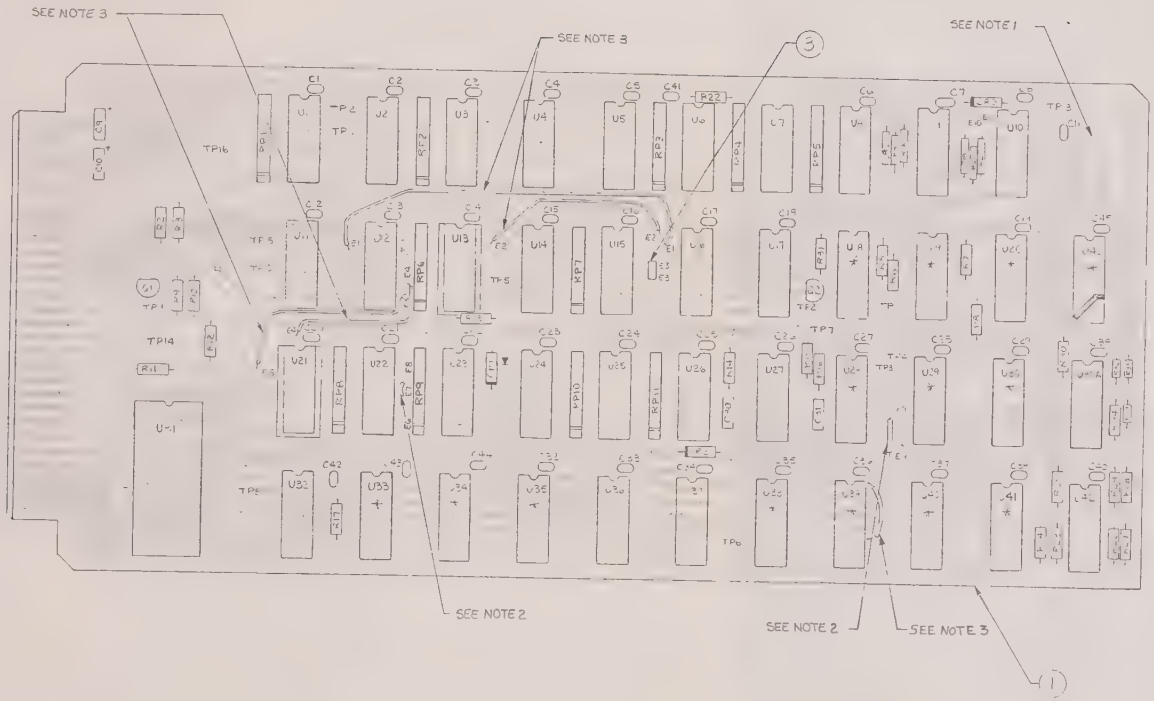
H	REV PER ECO # 02033 TMM 1-24-85	1-[illegible]-85	[illegible]	[illegible]
J	UPDATED PER DOC ACCURACY PROG PER ECO # 02178 6-28-85	7-29-85	[illegible]	[illegible]
K	UPDATED PER ECO # 02181 TB 8-8-85	8/19/85	PL	[illegible]
L	REV PER ECO # 02656 MCD 10/31/86	10/31/86	PL	[illegible]
L1	REV PER AUDIT # A0768 KKG 4-5-88			



1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. JUMPERS TO BE #24 AWG BUS WIRE WITH SLEEVING
3. JUMPERS TO BE #26 AWG INSULATED WIRE
4. * - DENOTES STATIC SENSITIVE DEVICE

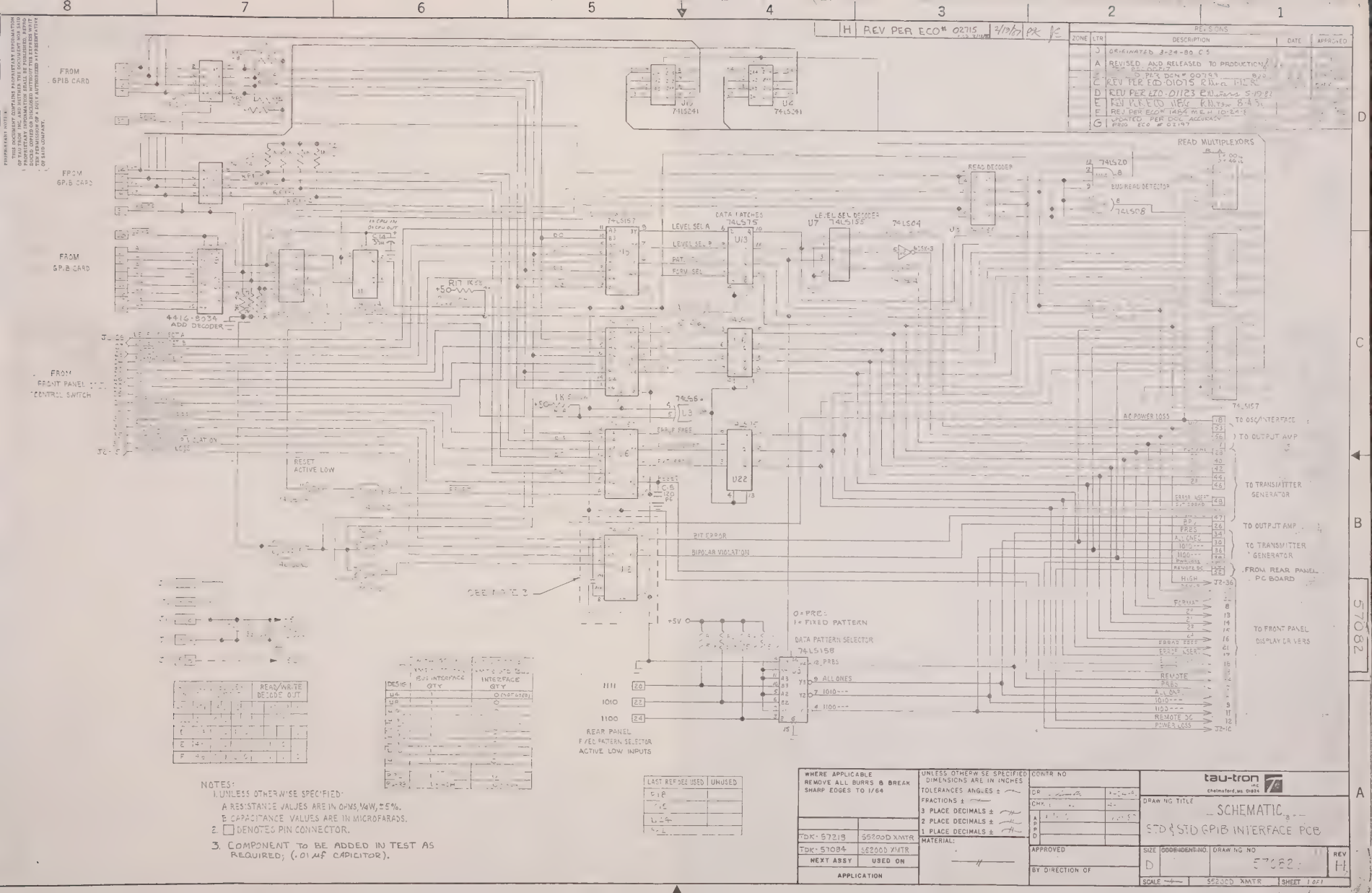
57010

J.T	12-31-80	ASSEMBLY
B. COOPER	1-9-81	TRANSMIT GENERATOR PCB
W. ROLLINS	1-20-81	

S5200D 7x

SEE P. L.
9545-7010

954 57010 L1



H REV PER ECO# 02715
REVISIONS
ZONE LTR DESCRIPTION DATE APPROVED
J ORIGINATED 3-24-80 C S
A REVISED AND RELEASED TO PRODUCTION
C REV PER ECO-01075
D REV PER ECO-01123
E REV PER ECO 1484 M E H 10-24-8
G UPDATED PER DOC ACCURACY PROG ECO # 02197
FROM GPIB CARD
FROM GPIB CARD
FROM GPIB CARD
FROM FRONT PANEL CONTROL SWITCH
4416-8034 ADD DECODER
RESET ACTIVE LOW
LEVEL SEL A
LEVEL SEL B
PAT.
FORM SEL
DATA LATCHES 74LS75
LEVEL SEL DECODER U7 74LS155
74LS04
74LS157
READ DECODER
74LS20
BUS READ DETECTOR
74LS08
READ MULTIPLEXORS
74LS157
AC POWER LOSS
TO OSC/INTERFACE
TO OUTPUT AMP
TO TRANSMITTER GENERATOR
TO OUTPUT AMP
TO TRANSMITTER GENERATOR
FROM REAR PANEL PC BOARD
TO FRONT PANEL DISPLAY DRIVERS
BIT ERROR
BIPOLAR VIOLATION
0 = PRBS
1 = FIXED PATTERN
DATA PATTERN SELECTOR 74LS158
PRBS
ALL ONES
1010---
1100---
REAR PANEL FIXED PATTERN SELECTOR ACTIVE LOW INPUTS
1111
1010
1100
READ/WRITE DECODE OUT
BUS INTERFACE QTY
INTERFACE QTY
NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A RESISTANCE VALUES ARE IN OHMS, 1/4W, ±5%.
B CAPACITANCE VALUES ARE IN MICROFARADS.
2. DENOTES PIN CONNECTOR.
3. COMPONENT TO BE ADDED IN TEST AS REQUIRED; (.01 μf CAPICITOR).
LAST REF DES USED
UNUSED
WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64
TDK-57219 55200D XMTR
TDK-57084 55200D XMTR
NEXT ASSY
USED ON
APPLICATION
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:
CONTR NO
APPROVED
BY DIRECTION OF
tau-tron inc
Chelmsford, Ma. 01824
DRAWING TITLE
SCHEMATIC
STD & STD GPIB INTERFACE PCB
SIZE D
CODE IDENT NO.
DRAWING NO 57082
REV H
SCALE
55200D XMTR
SHEET 1 OF 1
57082

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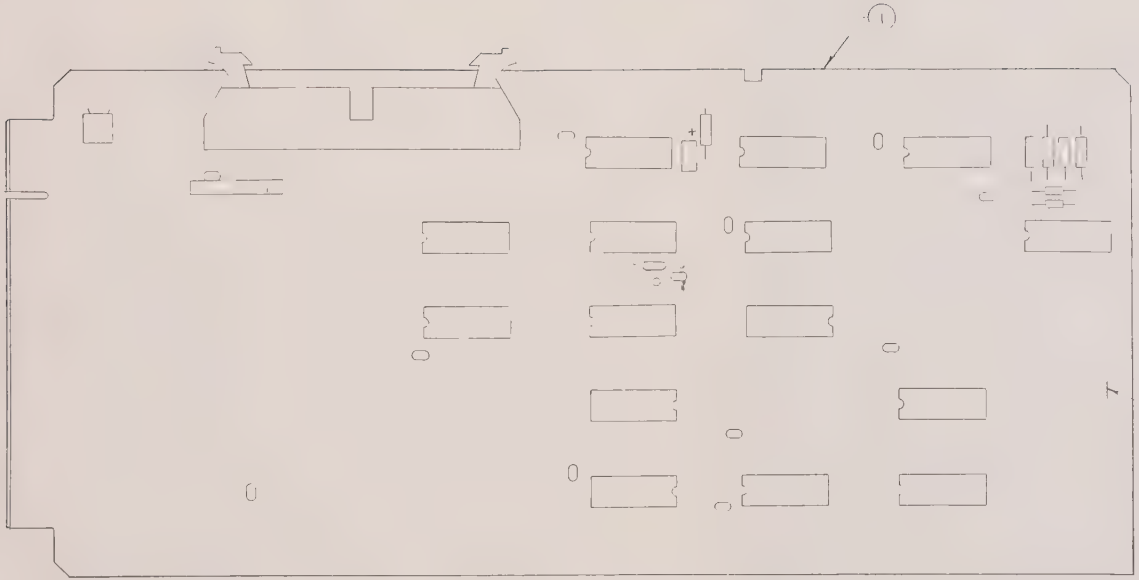
D	REDRAWN PER ECO 1123	R.N. 6-7-81
E	REV PER ECO-01130	8-4-82
F	REV PER ECO 1324	PRR 3-4-83
G	REV PER ECO #01484	M.E.H. 10-24-83
H	UPDATED PER DOC ACCURACY MOD ECO # 02197	



COMPONENT SIDE.

2. ~~⊖ DENOTES .01µF CAPACITORS.~~
3. PLACE A PERMANENT REVISION LEVAL MARK ON THE ASSY IN THE DESIGNATED AREA.
4. ~~ORIENTATE U13, U16, U22 IN OPPOSITE DIRECTION FROM REMAINING ICS.~~

57084

BUDD 1-2-81

WR 1-9-82

ASSEMBLY
STD BUS INTERFACE PCB.

E-57154 S52000 TX

945 57084 H

2-1 S52000 TX

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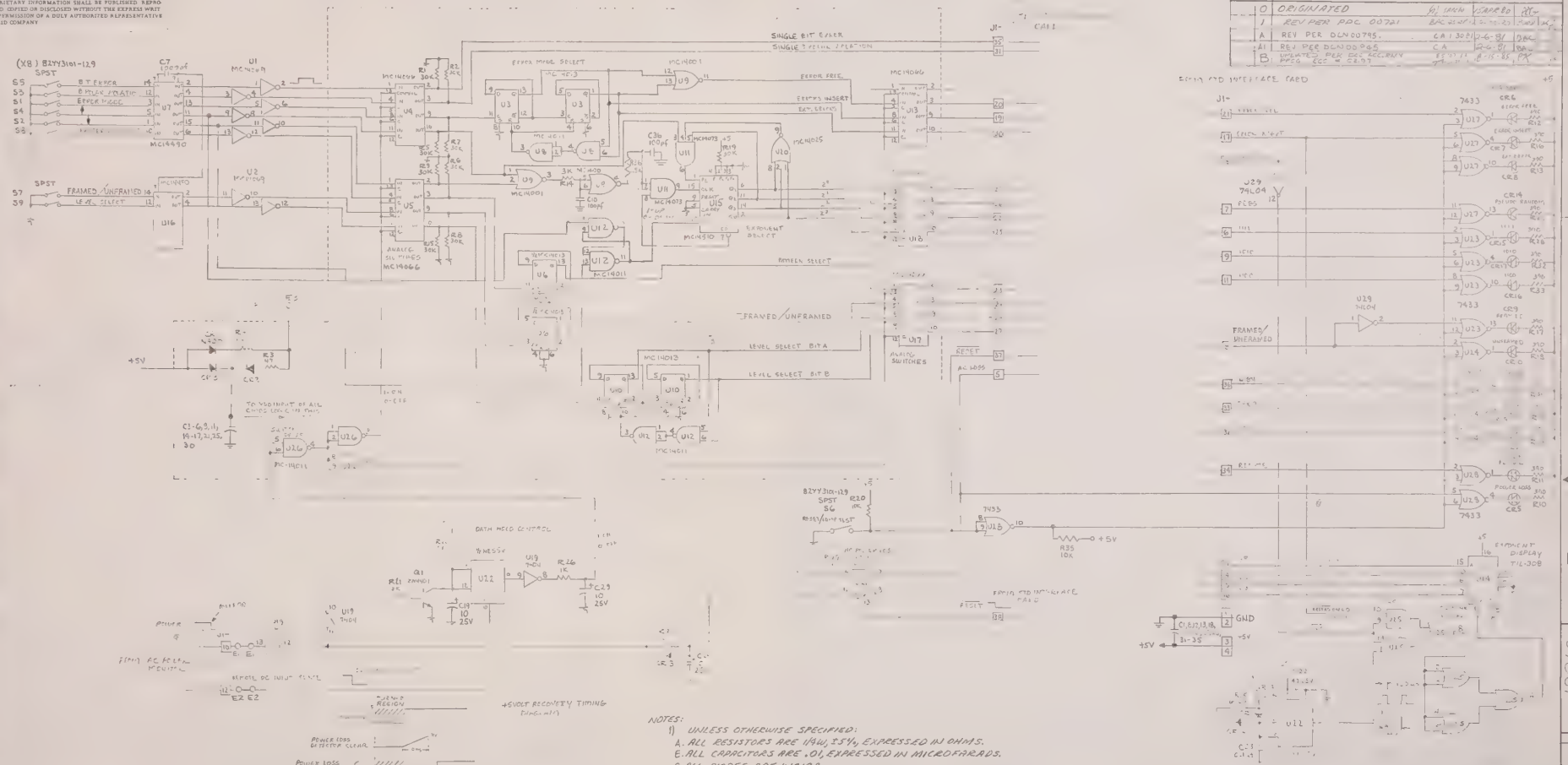
REV	LTR	DESCRIPTION	DATE	APPROVED
	0	ORIGINATED		
	1	REV PER PDC 00721		
	A	REV PER DLN00795.	6-6-81	
	A1	REV PER DLN00966	6-6-81	
	B	UPDATED PER ECO ECC-RUN PECO ECO # 5297	8-15-85	



NOTES:
1) UNLESS OTHERWISE SPECIFIED:
A. ALL RESISTORS ARE 1/4W, ±5%, EXPRESSED IN OHMS.
B. ALL CAPACITORS ARE .01, EXPRESSED IN MICROFARADS.
C. ALL DIODES ARE IN4148.

LAST	REF DES	
U29	CR20	C
Q	S9	C36
R36	E3	

TT 5200D	5200DTX
NEXT ASSY	USED ON
APPLICATION	

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL

CONTR NO
DR
CHK
APPD
APPROVED
BY DIRECTION OF

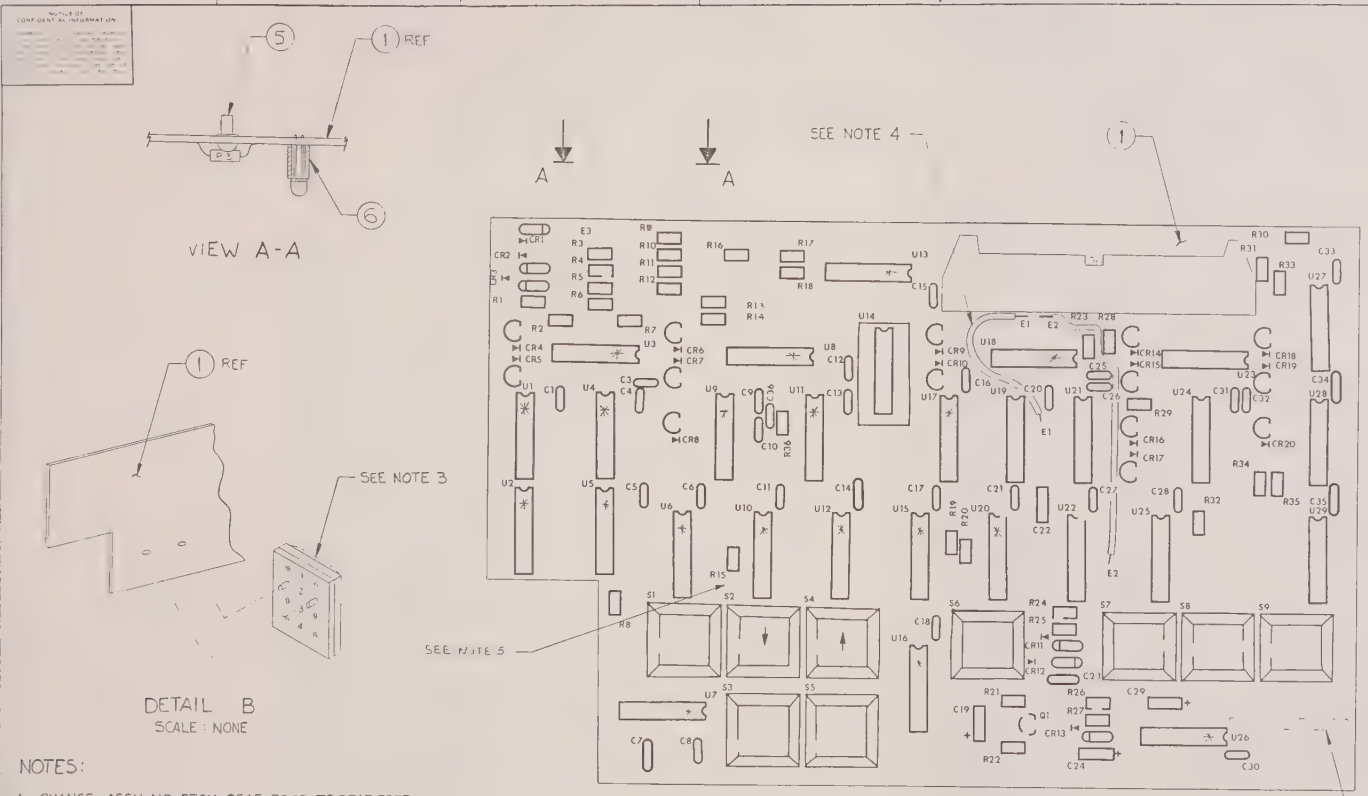
tau-tron inc
Chelmsford, Ma 01824

DRAWING TITLE
5520OD TRANSMITTER

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D		57067	B
SCALE	5520OD	SHEET 1 OF 1	

57067

B	REDRAWN, WITH CHANGES REPLACES REV A #02221	
C	REV PER ECO # 02575	
D	REV PER AUDIT # A0469	



NOTES:

1. CHANGE ASSY NO FROM 9545-7069 TO 9545-7355. THEN PLACE THE ASSEMBLY PARTS LIST REVISION, ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. STATIC SENSITIVE DEVISES ARE DENOTED BY THE SYMBOL " * ".
3. INSTALL S1 THRU S9 (SWITCHES), AFTER WAVE SOLDERING, WITH PIN LOCATIONS AS SHOWN IN DETAIL B. CLEAN WITH ISOPROPYL ALCOHOL.
4. UNLESS OTHERWISE SPECIFIED THE FOLLOWING APPLY;
   A) ALL JUMPER CABLES TO BE #26 AWG STRANDED INSULATED WIRES.
   B) ALL LED's REQUIRE SPACERS.
5. REMOVE R1 LOCATED UNDER R15 ON COMPONENT SILK SCREEN.

D-57360	55200E TK
NEXT ASSY	USED ON
APPLICATION	

TOLERANCES ANGLES ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL

C.S.	9/20/80	
R.B.	9/25/80	
W.E.L.	9/25/80	

tau-tron

TRANSMITTER FRONT PANEL CONTROL & DISPLAY PCB

SIZE	CODE IDENT NO	DRAWING NO	REV
D		57355	C1

SCALE 2/1 | 55200E | SHEET 1 OF 1

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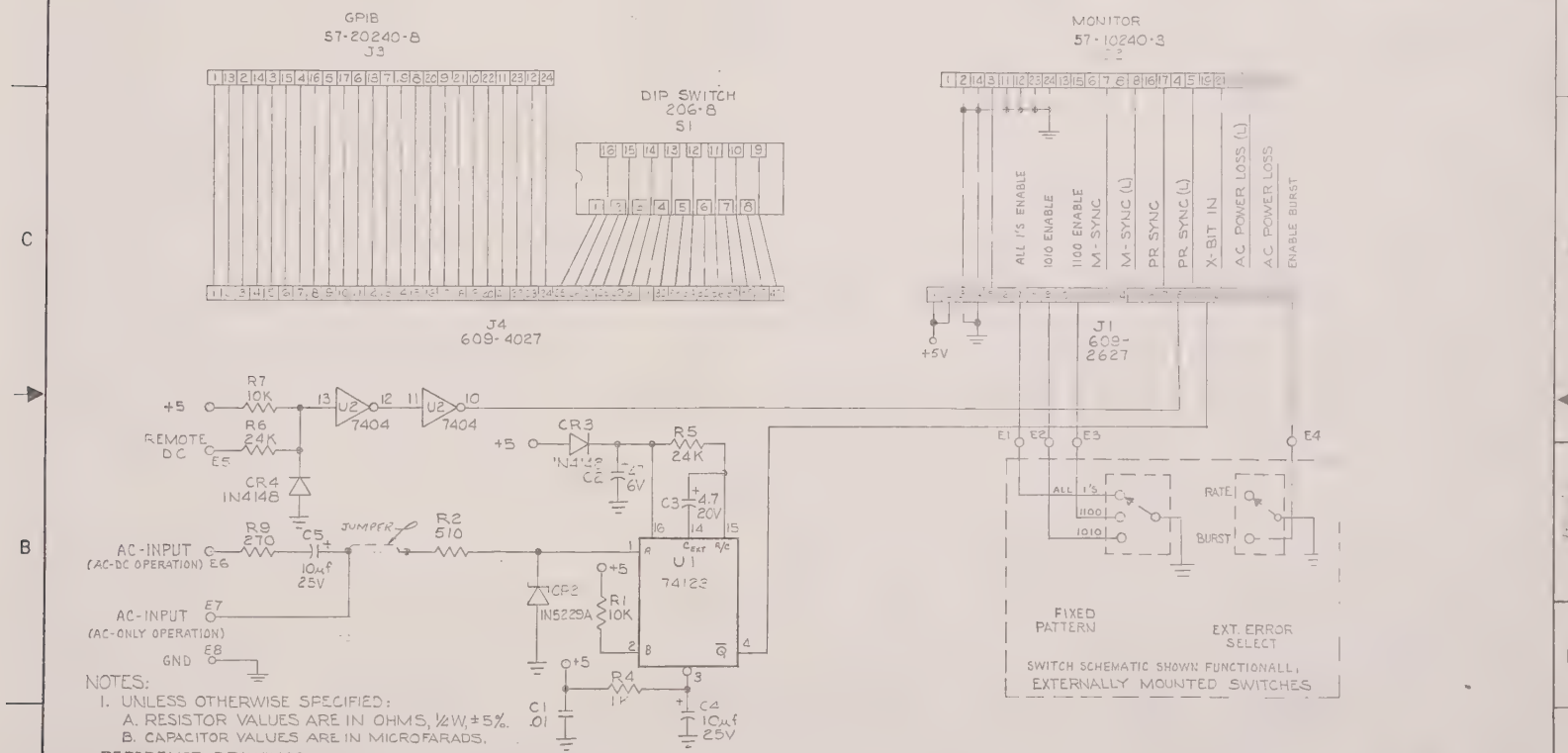
OF SAID COMPANY

REVISIONS		
ZONE	LTR	DESCRIPTION



NOTES:
1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTOR VALUES ARE IN OHMS, ½W, ±5%.
   B. CAPACITOR VALUES ARE IN MICROFARADS.

REFERENCE DRAWINGS:
ASSEMBLY: TCK-57186
FABRICATION: TCG-57185

U2	
S1	
R9	
J3	
CR4	R3.8
C3	CR1
LAST USED	UNUSED
APPLICATION	

WHERE APPLICABLE:
REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64

C-57186	S5200C TX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES. ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR NO.
DR 9-3-80
CHK B. Cooper 9-15-80
APPD 9-18-80
APPROVED
BY DIRECTION OF

tau-tron INC
Chelmsford, MA 01824

DRAWING TITLE
SCHEMATIC
TX REAR PANEL INTERFACE BOARD

SIZE	CODE IDENT NO.	DRAWING NO.	REV
C		57184	C

SCALE — | S5200C TX | SHEET 1 OF 1

07-0045 (4/71) .003 MYLAR

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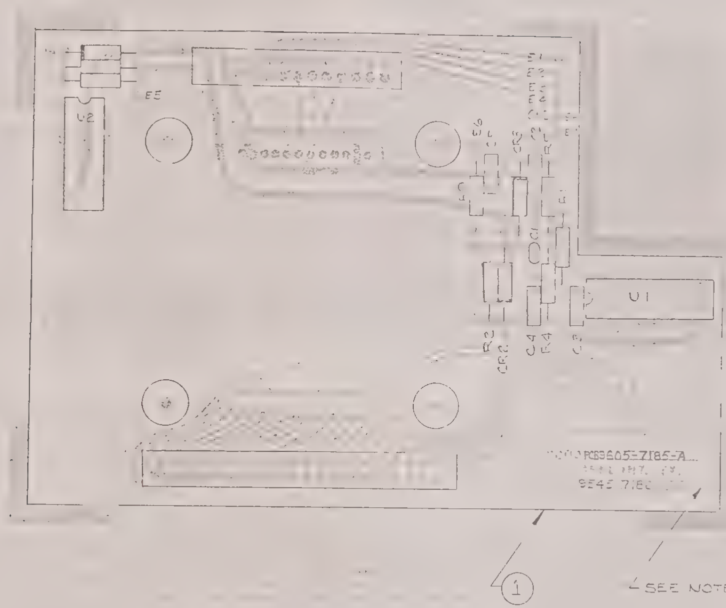
O	ORIG. PER PDC 00718
1	REV PER PDC [illegible]
A	REV PER [illegible] C.S. 12-29-80 1-6-81 BAC
B	REV PER [illegible] C.S.
[illegible]	REV PER ECO# 01735 M.E.H 2-21-84
D	UPDATED PER DOC ACCURACY PROG ECO # 02197



NOTE:
1. [illegible]
2. SEE SHEET 2 FOR [illegible]
3. WIRING SIDE.
[illegible] ON THE ASSY IN THE DESIGNATED AREA.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

E. Cooper 9-15-80

9-15-80

D-57144 SS200C TX

ASSEMBLY
REAR PANEL INTERFACE TX

C 954 57186 D

SS200C TX

OF 2

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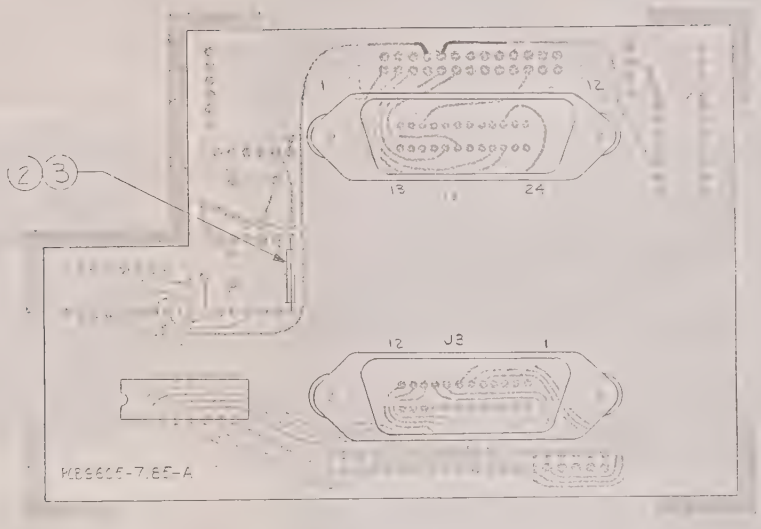
SEE SHEET [illegible]



WIRING SIDE

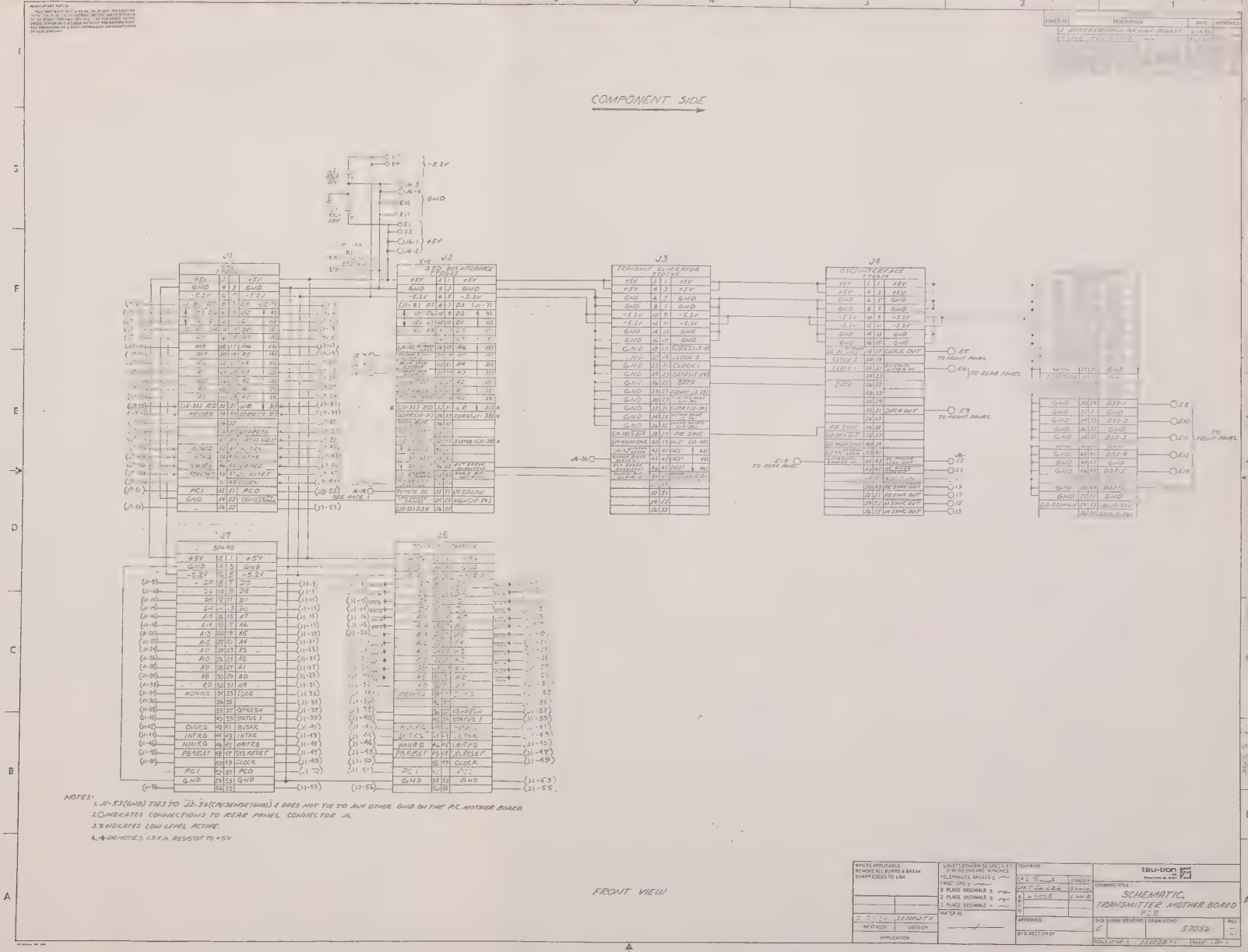
B. Cooper

ASSEMBLY
REAR PANEL INTERFACE TX

D -57144 S5200D TX

C 954 57186 D

2/1 S5200D TX


COMPONENT SIDE
J1
J2
STD BUS INTERFACE
J3
TRANSMIT GENERATOR
J4
OSC INTERFACE
J7
SPARE
J8
TO FRONT PANEL
TO REAR PANEL
FRONT VIEW
NOTES:
1. J1-53(GND) TIES TO J2-54(CPUSENSE(GND)) & DOES NOT TIE TO ANY OTHER GND ON THE P.C. MOTHER BOARD.
2. INDICATES CONNECTIONS TO REAR PANEL CONNECTOR J6.
3. * INDICATES LOW LEVEL ACTIVE.
4. ✦ DENOTES 1.5 K.Ω. RESISTOR TO +5V
WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
2 PLACE DECIMALS ±
3 PLACE DECIMALS ±
MATERIAL
NEXT ASSY
USED ON
APPLICATION
CONTRACT NO
APPROVED
BY DIRECTION OF
tau-tron
DRAWING TITLE
SCHEMATIC, TRANSMITTER MOTHER BOARD PCB
SIZE
CODE IDENT NO
DRAWING NO
57056
REV
SCALE NATURE
SHEET 1 OF 1

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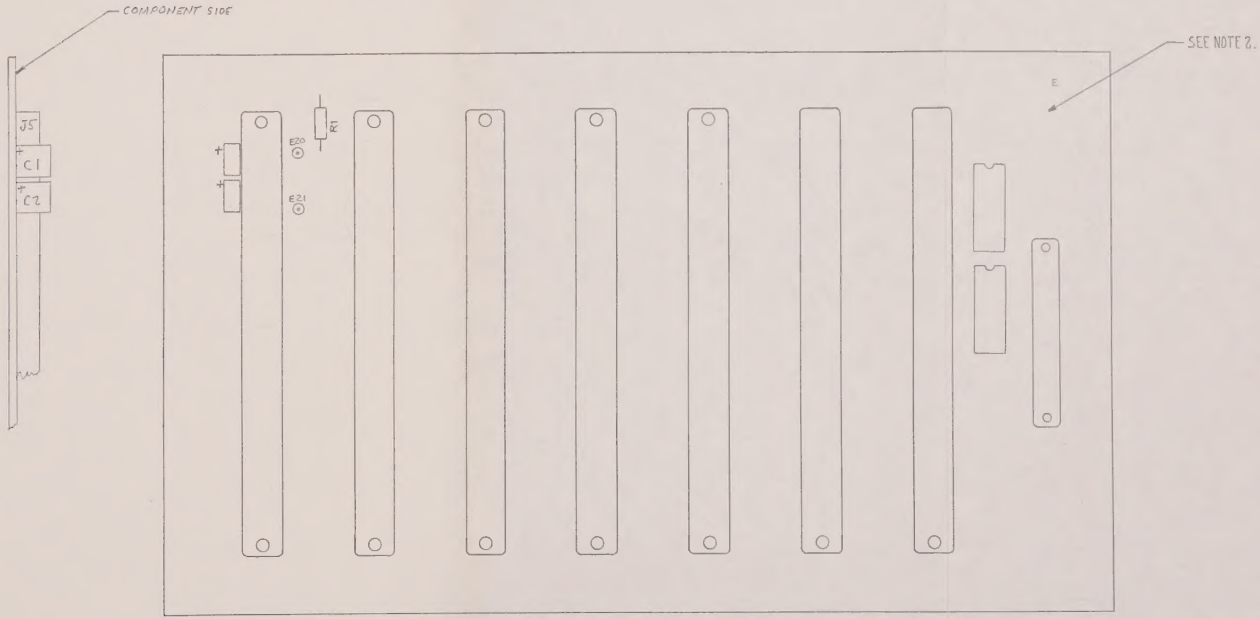
C	REDRAWN PER ECO# 01123 B.M.B 5/5/82	5/6/82	[illegible]
D	REV PER ECO-1171 RNunch	1-19-82	[illegible]
E	REV PER ECO-01253 RZ 27 OCT 82	10-29-82	AAM
F	UPDATED PER DOC ACCURACY PROG ECO # 02197 [illegible]	8/16/85	PK



NOTES:
1. ~~UNLESS OTHERWISE SPECIFIED:~~
~~A) RESISTANCE VALUES ARE IN OHMS 1/4W ±5%~~
~~B) CAPACITANCE VALUES ARE IN MICROFARADS.~~
2. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

C. SMITH 1-8-81
B. COOPER 1-12-81
W. ROLLINS 1-14-81

ASSEMBLY
TX MOTHER BOARD P.C.B.

57377 S5200E TX
57058 S5200D TX

57058 F

2/1 S5200D TX

10 Lyberty Way, Westford, MA 01886
(617) 692-5100 Telex 750245 1-800-TAU-TRON (Outside MA)